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SERIAL NUMBER 10/695,803	FILING DATE 10/30/2003  RULE	CLASS 438 716	GROUP ART UNIT 2825	ATTORNEY DOCKET NO. Q77571
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## APPLICANTS

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\*\* CONTINUING DATA \*\*\*\*\*

*JSH*  
*NONE*  
*JSH*  
*NONE*

\*\* FOREIGN APPLICATIONS \*\*\*\*\*

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 01/29/2004

Foreign Priority claimed	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY	SHEETS DRAWING 5	TOTAL CLAIMS 45	INDEPENDENT CLAIMS 3
35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	CA			

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## TITLE

Method for clock synchronization validation in integrated circuit design

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